Form PTO-1449	ATTORNEY DOCKET NO.	SERIAL NO.
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)	**AUS920030544US1**	**Not Assign d**

APPLICANT **DeWitt, Jr. et al.**	
FILING DATE	GROUP ART UNIT **Not Assigned**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
W	AA	5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989
W	AB	6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999
W	AC	6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000
W	AD	6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999
W	AE	6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998
W	AF	5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997
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W	AH	6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998
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W	AJ	5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995
W	AK	5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997
W	AL	6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998
W	AM	5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995
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W	AU	4,291,371	Sep. 22, 1981	Holtey	364/200	Jan. 2, 1979
W	AV	5,938,778	Aug. 17, 1999	John, Jr. et al.	714/45	Nov. 10, 1997
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Form PTO-1449	ATTORNEY DOCKET NO. AUS920030544US1	SERIAL NO. Not Assigned
LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*		
	APPLICANT **DeWitt, Jr. et al.**	
	FILING DATE	GROUP ART UNIT **Not Assigned**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
W	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
W	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
W	BD	2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
W	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
W	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
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EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	NO
—	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38		X
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	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.
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W	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performanc Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

RELATED PATENT APPLICATIONS

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
W	BP	09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
W	BQ	08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Ev nt S quenc using an Eff ctiv Address in a Proc ssing System	Oct. 2, 1995

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Form PTO-1449	ATTORNEY DOCKET NO. AUS920030544US1	SERIAL NO. 10/757,248
LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*		
	APPLICANT **DeWitt, Jr. et al.**	
	FILING DATE **January 14, 2004**	GROUP ART UNIT **2183**

RELATED PATENT APPLICATIONS					

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
W	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
W	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
W	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
W	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
W	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
W	AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
W	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
W	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
W	AI	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
W	AJ	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
W	AK	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
W	AL	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
W	AM	10/757,212 / AUS920030553US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Based on Arithmetic Combinations of Performance Counter Values	Jan. 14, 2004
W	AN	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004

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